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Design and Implementation of Hybrid FIR Filters using Vedic Multiplier and Fast Adders

S. Jayakumar, R.Selvam, K. Karthikeyan, R. Natesan

Abstract: FIR (Finite Impulse Response) filters play a significant role in the field of Digital Signal Processing (DSP) to eliminate noise suppression in Electro Cardio Graph (ECG), Imaging devices and the signal stored in analog media. So filter evaluation is accomplished to reduce the noise level. The Filter passes only the desired frequency to pass thereby reducing distortion in the processed signal during measurement. The FIR filter comprises of basic units like adders, multipliers and the delay element for its operations.FIR and IIR are the two types of digital filters chosen based on the range of inputs, complexity and size requirement. Multipliers and adders play a vital role in deterring the performance of FIR filter. In this work, we design and analyze different multiplier and adder for high-performance Fir filter implementation. The Vedic Mathematics is the methods containing 16 Sutras to aid fast mental calculations. In this work, we propose modified Anurupye Vedic multiplier methods with Kogge Stone fast adder for implementation in the direct form FIR filter. This approach provides 1.5% decrease in delay and 10.2% reduced in power, hence increasing speed marginally than previous methods. Along with low power consumption in Very High-Speed Hardware Description Language (VHDL), all the adders and the multiplier topologies are Synthesized using (Xilinx Spartan – 6 FPGA) Trainer Kit and the proposed 8 – Tap FIR filter is executed using this Board.

Index Term: FIR, Anurupue Vedic Multiplier, Kogge Stone adder, VHDL, Xilinx Spartan –6 FPGA.

I. INTRODUCTION

FIR is the most significant operations used in speech processing, loudspeaker, ECG and in various and in various digital signal processing applications. The Digital filters are employed most often as they operate with numbers instead of physical quantity. The requirement of high-speed processing increases with the innovation in the signal processing applications. From **[10]** the statisticsshow that 70% of instruments are used to perform basic operations like adders and multiplications. Digital Filters used are classified into FIR and IIR filters. [**11**] FIR filter is mostly used algorithm by providing no redundant computation. The inmost heart of every processing unit is its data path involved in its operations. In turn, the data path relay on basic arithmetic like multiplier and adder units since this Operations increases the complexity of filter components study of various multipliers with Vedic methods and fast adders are analyzed for effective implementation. The old Vedas are assigned Rig, Yajur, Sama and Atharva.The old methodsof Vedic Mathematics from [9] and [3] have reinventedfromAtharvaVedas including thestorehouse of information by Sri Bharati Krishna Tirthaji(1884-1960).The improvement of Vedic Sutura lies in the way that it diminishes the whimsical articulation in standard number shuffling to a less troublesome and profitable way. The Vedic formulae depend upon the "trademark benchmarks on which the human character works". Vedic science is rotated based around 16 Sutras administering various parts of science like math, polynomial math and geometrycalculations. These strategies and approach can be used to trigonometry, conics, assessment and applied number shuffling including unquestionable techniques.The Vedic course of action manages badly designed issues, or colossal wholes can typically be effectively wrapped up by this procedure. Such procedures are a tad of an entireplan of math which is more unsurprising than the contemporary framework. It shows the reliable and combined structure of science with snappy and less mind boggling implementation.latency and throughput wandered from standard development process. The figuring gives a stage to settle DSP development sensibly and rapidly for a little length of movements. The stimulations are done in Xilinx test system and acknowledged in more diminutive scale wind setup test system for researching power.[8] has proposed anexperiment on various adders and multipliers Using a Carry Look Ahead snake, Carry spare snake, Vedic multiplier and Wallace multiplier, are poverty stricken down to display new FIR structures. The proposed structure is Carry Look Ahead snake and Wallace tree multiplier utilized for execution of the FIR channel. To assess FIR execution he utilized Xilinx ISE 14.4 (Verilog HDL). [12] Has comfortable another course of action with have low multifaceted nature FIR channel. From the outcomes, the FIR Wallace Carry Save structure incorporates less memory with less power likewise at any rate more when separated from FIR Wallace Carry Skip structure when examined different streets concerning Xilinx synthesizer. [1] Has built up a FIR channel to improve the demonstration of the structure by utilizing the Urdhava-Tiryagbhyamalgorithm. He figured farthest point with improved speed than different frameworks. To decrease figuring time better among the different calculations, direct convolution by utilizing MATLAB is the coretechniquesutilized in the FIR framework.

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* Correspondence Author
S. Jayakumar*, Assistant Professor (S.G),Department of E.I.E ,Karapakkam, Chennai, India
R.Selvam, Assistant Professor ,Department of E.E.E ,Karapakkam, Chennai, India
K. Karthikeyan, Associate Professor ,Department of E.I.E ,Karapakkam, Chennai, India
R. Natesan, Assistant Professor (S.G),Department of E.E.E ,Karapakkam, Chennai, India

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UrdhvaTiryakbhyam is the common multiplication procedure appropriate to the entire forms of multiplication. It literally denotes "Vertically and crosswise" operations to be performed. Nikhilam Sutra indicates "all from 9 and lasts from 10" though it applies to all forms of multiplication, provides efficient services for larger numbers. The drawbacks of this method are overcome by using Anurupye method. The demand for a fast Parallel Prefix Adder (PPA) is used principally for its speed of operations over ripple carry adder [4]. PPA is a family unit of adders resulting from the frequently known carry look ahead adders. These adder methods are suitable for wider word length addition with the use of tree network to reducing the latency to 2On (log) where 'n' signify the number of bits used.In this paper this paper we propose Kogge Stone fast adder and modified Anurupye Vedic multiplier methods to implement High-performance FIR filters as modified with the our previous work of FIR implementation with Anurupye multiplier. The paper is organised as: The proposed FIR filter and its operations are explained in Section II. Directs depictions of the Anurpye Vedic multiplier are demonstrated in Section III. Outline of Parallel adder utilising Kogge Stone algorithm is described in Section IV. In Section V the results of proposed FIR filter results are simulated and implemented in FPGA implementation of FIR filter. Section VI depicts the Conclusion of obtained results.

II. LITERATURE OVERVIEW

Relating to multi-antenna systems is considered by author [1], it gives significant power consumes than single antenna model by the diversity techniques. In [2] author the proposed research only on physical layer routing, where as in contrast the authors [3],[4],[5] the cooperative routing protocol, is based on both network layer and physical layer routing , particularly in [3] the author considered static cooperative routing in Ad hoc MANET wireless networks. In [4], the author developed the cooperative routing by using multi-source multi-destination network, to achieve energy savings, and this is the extension of [3].H. Shi, T. Abe [5] the distributed cooperative routing is simulated .The Biswas and R. Morriset al., developedEXOR extension opportunistic proactive routing protocol, in which a lot of packets are conferred, by a sender (for instance 10-100 packets for each batch).List of sent focus focuses are kept up ETX [7].In [8] proposed another routing protocol is known as a "Simple Opportunistic Adaptive Routing protocol"(SOAR) .In [9] creator make the way trade of, in context on focuses instead of ' ricochet' framework called need based sending. Furthermore, for unicast the ExOR calculations is well is reasonable , yet for multicast EXOR won't perform well it may not perform well,Chachulski et al. [10] said well suit .At last, piggybacked ACKs may lose, so' reproduced trans-mission may occur. The creators Zehua Wang and Yuanzhu Chen et al., in [11]proposed a 'Satisfying Opportunistic Routing in Mobile Ad Hoc Networks' (CORMAN) new calculation , maintain a strategic distance from the recreated transmission when gathering is lost ,in adaptable Ad Hoc structures. In an altogether earth shattering environment of flexible Ad Hoc systems, the multicast transmission, simultaneously a proportional information bunch has send to recipients in the MANET. As a rule, convey information packets consumes power and move speed and which ought to be avoided[12],[13] by utilizing multi-hurling in MANETs with an extraordinary piece of the time changes topology environment, i.e., focus focuses in the MANET, at whatever point , may join or withdraw the get-together. Xiao Chen and JieWn et al [14] evaluated multicast protocols in conservative adhoc organize .After discover the limitations of two wired multicast tree routing protocol ,producer examined four Ad Hoc multi cast protocol what's more broke down two issues i.e., QoS multicast and strong multicast . C. - C. Chiang, M.Gerla, and L.Zhang et al [15] producer applied 'Sending Group Multicast Proto-col'(FGMP), when packets sending this protocol keeps in track with get-togethers of focus focuses not of connections. To stay away from, copy date bunch the sending Nodes in FG set only gets the information packets, and it will convey. In any case, in FGMP the tremendous issue is selection and keep up the set FG of sending focus focuses. Devarapalli, and D.Sidhu et al [16], the multicast routing protocol is proposed by Zone based. To maintain a strategic distance from the above issues, here source started on-demand protocol. In which a multicast conveyance tree is made by utilizing zone routing plan, from this time forward it is a source tree based proto-col. In this protocol topology changes can be constrained, rather spread it through the entire of the network. In [17], ODMRP (On Demand Multicast Routing Protocol) finds the sending focus point among the got information bunch focus point in the system, to beating. Only sending focus will transmit the multicast messages to a party part, by then makes a joint table message, and convey to its neighboring focus focuses. This neighbor focus establishes sending way. In [18] MAODV,(Multicast On-demand Distance Vector Routing Protocol) convey the course demand ,on demand basics only the way is found and constructs a routing tree.

III. PROPOSED SYSTEM

A FIR filter is a non-recursive automated filter with, no examination exists, and the yield exists just for the present and the past estimations of the information signal [7]. In the FIR filter, impulse reaction gives limited no of zeros for the term of 0 to N – 1 by then crown jewels to zero. The yield condition of the FIR filter is the resultant of the convolution of two sign k (n) as the dedication of the filter to be attempted, g (n) yield of the data sign to the FIR filter got by duplication with the coefficient of filter j (n).

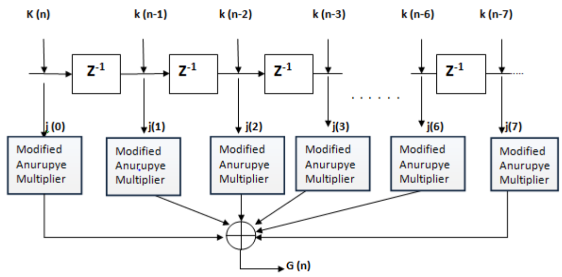

Figure 1. Proposed Block Diagram of Linear Phase FIR filter

$$g(n) = j(n) * k(n) \quad (1)$$
$$g(n) = \sum j(n) * k(n-k) \quad (2)$$

In this work, a novel architecture design using Modified Anurupye multiplier (Anurupye Vedic multiplier and Kogge Stone adder) is proposed as shown in Figure 1. The conventional FIR method consists of delay elements to sum the delay of the signal with individual values by multiplying the result of the preceding steps with the resulting coefficient. These filter operations can be optimized by using Anurupye methods to calculate the products and sum the given values using kogge Stone multiplier as shown in Figure. The main feature of this approach is to use fewer resources for performing the same task by providing performance metrics.

IV. METHODOLOGY

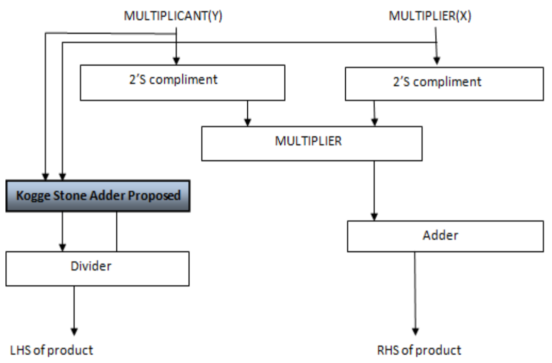

Figure.2.Architecture of modified Anurupye Vedic Multiplier

This method is a part of Vedic multiplier suitable whenever given numbers is not closer to the power of 10. The another method in Nikhilam failed to perform this operation but appropriate only for numbers close to each other. The Anurupye multiplier is much easier to understand with the prerequisite of two techniques of multiplication, Vinculum process and Nikhilam method.Tirthaji Maharaj has tricks to multiply numbers using Vedic Mathematics and classified it into Specific and General Methods [12]. The accurate method includes EkayunenaPurvena, Antyaordaske, Nikhilam Sutra and Anurupyena Sutra and general methods using UrdhvaTiryak Sutra and Vinculum Process.Nikhilam Sutra multiple numbers similar to 97 & 95, 998 & 986, 101 & 114, 994 & 1006 i.e. numbers closer to power of 10 [11]. Using this Sutra is a specific Method of multiplication in Vedic Mathematics which denotes shortcuts methods for multiplying numbers closer to the power of 10 (10, 100, 1000, etc.) as we have mentioned in our previous work [5]. Figure 2 shows the proposed modified Anurupye structure with Kogge Stone adder.

Steps to compute Anurupye Algorithm: Working Base (W.B.) concept: For example, consider Multiplication of Numbers like 63 & 67.

1. Since both the numbers (63 & 67) are closer to 60, we find working base as 60 (6*10) as an alternate of 100, with a factor of 6.
2. Apply Nikhilam Sutura concept i.e. taking numbers 3, 7 as they are greater to 60 the working base.
3. Multiply 3 and 7 to get the result as 21. As the base is 10, we need exactly onedigit in the first half, and hence the carry is forwarded to next stage.
4. Cross Add either 63 & 7 or 67 & 3 resulting with 70. In Anurupyena Sutra, first, multiply the result of cross addition with a factor (6) to get 420 before adding carry with value 2 with a final answer of 4221.

V. ALGORITHM

I) Kogge Stone Algorithm

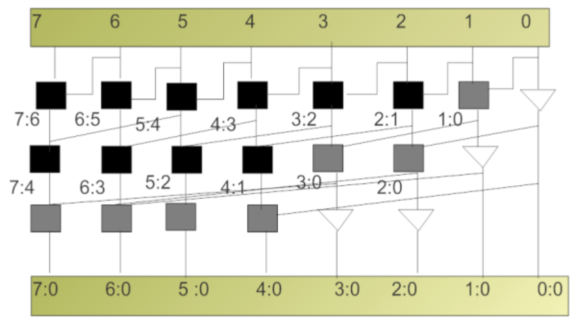

Figure 3. Structure of 8-bit Kogge Stone Adder

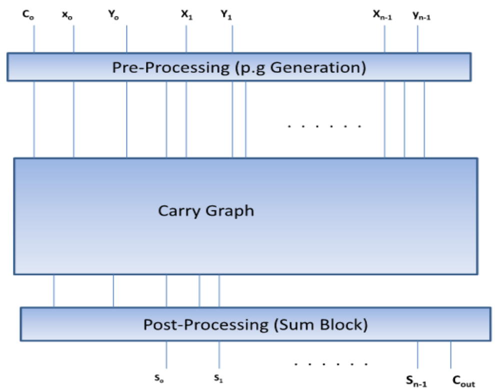

Figure 4. Block Diagram of Kogge Stone Adder

The [5] functioning can be quickly processed in three Stages for manipulating Sum Bits such as Figure 3 and 4.

- Pre-processing

Preprocessing involves steps to calculate generate a signal and propagate values resultant to each pair of input bits A and B.

$$P_i = A_i \text{ xor } B_i \quad (3)$$
$$G_i = A_i \text{ and } B_i \quad (4)$$

- Carry look ahead network

This Stage is the heart part of KSA which differentiates with other adders.This unit forms the main reason for high performance. This Block calculates the resultant to each bit. After generating carries in parallel, they are portioned into separate blocks. It utilisesCarry propagate with generating signals as intermediate signals which are specified by the logic expression below:

$$P_{i:j} = P_{i:K+1} \text{ and } P_{K:j} \quad (5)$$
$$G_{i:j} = g_{i:k+1} \text{ or } (P_{i:k+1} \text{ and } G_{k:j}) \quad (6)$$

- Post processing

This unit is the final step, and the operations are similar to the family of Carrying Look Ahead adder to compute sum bits.

The logic to generate sum bits are given by

$C_{i-1} = (P_i \text{ and } C_{in}) \text{ or } G_i$ (7)

$S_i = P_{i\,x} \text{ or } C_{i-1}$ (8)

VI. RESULT AND DISCUSSION

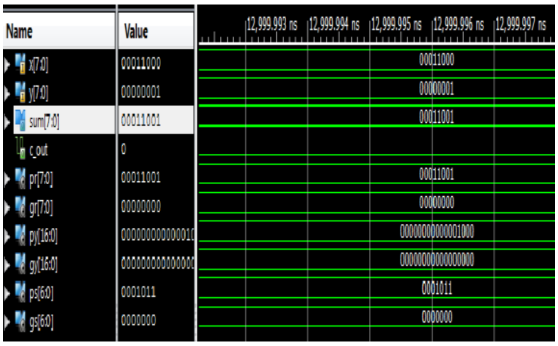

Figure 5. Simulation result of 8-bit Kogge Stone

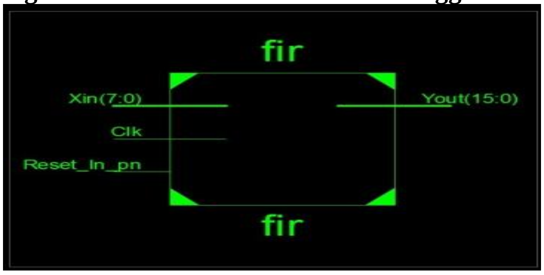

Figure 6. FIR Top Module Schematic output

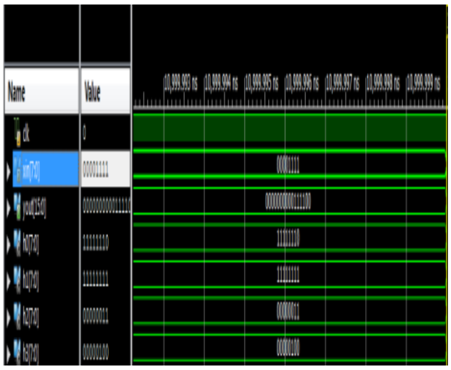

Figure 7. Simulation result of FIR output

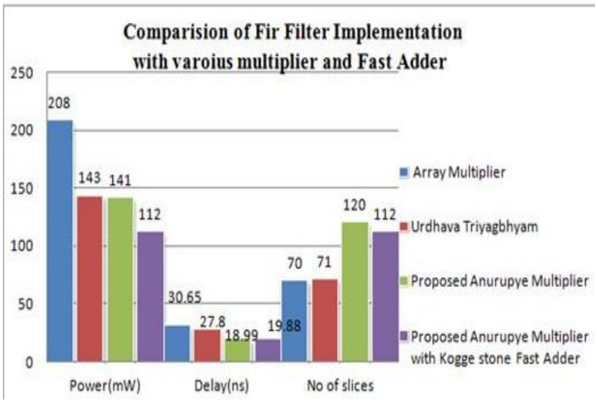

Figure 8. FIR Filter output

TABLE I: Vedic Multipliers Comparisons

Multipliers		Memory	Power	Delay
Array		196mb	56mW	22.05ns
Vedic	UrdhavaTriyagbhyam	194mb	52 mW	25.42ns
	Anurupye	191mb	34 mW	20.54ns

TABLE II: Comparison Of Fast Adders

Adder	Power(mW)	Delay(ns)
8-bit Carry Ripple Adder	0.875	72.1
8-bit Carr Save Adder	1.575	20.48
Kogge Stone Adder Proposed	0.014	18.56

TABLE III: Comparison of Fir Filter

Multiplier	Power(mW)	Delay (ns)	No of slices
FIR Implementation/Existing (Array)	208	30.605	123
FIR Implementation/Existing (UrdhavaTriyagbhyam)	143	27.801	133
FIR Implementation (Anurupye Multiplier)	14	18.99	120
FIR Implementation Proposed (Anurupye Multiplier &Kogge Stone Adder)	14	17.47	182

The Simulation result for FIR filter is shown in Fig 7 in which 8-bit value of xin [7:0]- 00001111 and ho-h7 with different combinations of 8 bits and result is16 bit yout=0000000000111100 is obtained. The Figure 8 Shows the comparison result of FIR filter. Table me the result of Vedic Multipliers Comparisons regarding Memory, Power and Delay of Various Multipliers. Table II Shows the result of Fast adders Comparisons was regarding Delay, Power and number of slices using several Fast adders.

Table III Shows the result of proposed FIR filter using proposed Vedic multiplier and a fast adder.

VII. CONCLUSION

In this article develop a proposed algorithm Multicast O-CORMAN for MANETs. The multipath routing for MANET have superior performance measured in Throughput, Total Consumed Power and Control overhead In future, this algorithm can be extended by using multi flow data transfer and also change the pause time to evaluate the performance of algorithm

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